

Single-Mode Monolithic Quantum-Dot VCSEL in $1.3 \mu\text{m}$ With Sidemode Suppression Ratio Over 30 dB

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Abstract—We present monolithic quantum-dot vertical-cavity surface-emitting lasers (QD VCSELs) operating in the $1.3\text{-}\mu\text{m}$ optical communication wavelength. The QD VCSELs have adapted fully doped structure on GaAs substrate. The output power is $\sim 330 \mu\text{W}$ with slope efficiency of 0.18 W/A at room temperature. Single-mode operation was obtained with a sidemode suppression ratio of $>30 \text{ dB}$. The modulation bandwidth and eye diagram in 2.5 Gb/s was also presented.

Index Terms—Bandwidth, quantum dots (QDs), single mode, surface-emitting laser.

I. INTRODUCTION

VERTICAL-CAVITY surface-emitting lasers (VCSELs) at around $1.3 \mu\text{m}$ fabricated on GaAs substrates have been expected to realize high-performance and low-cost light sources for fiber-optic communication systems. The large conduction band offset improves the temperature performance over that of conventional InP-based materials. The GaAs system provides high-performance AlGaAs–GaAs distributed Bragg reflector (DBR) mirrors and permits the use of the well-established oxide-confined GaAs-based VCSEL manufacturing infrastructure. So far, the most promising materials at $1.3 \mu\text{m}$ on GaAs substrate were GaInAsN quantum wells [1]–[3] and quantum dots (QDs) [4]–[6]. Each of these solutions offers some advantages over its counterpart, but further extending the emission wavelength is relatively easier in QDs than GaInAsN. QD edge-emitting lasers have also been proven to exhibit excellent performance characteristics, including low threshold current, temperature insensitive threshold current, and high differential gain. For VCSELs, QDs [7] are particularly advantageous, as nonequilibrium carriers are localized in the QDs and, thus, spreading of nonequilibrium carriers out of the injection region can be suppressed. This may result in ultralow threshold currents at ultrasmall apertures [7], [8].

Recently, $1.3\text{-}\mu\text{m}$ QD VCSELs using intracavity structures have been proposed [9], [10]. However, devices with a monolithic structure are always desirable. In this letter, we demonstrate monolithically single-mode QD VCSELs with high side-

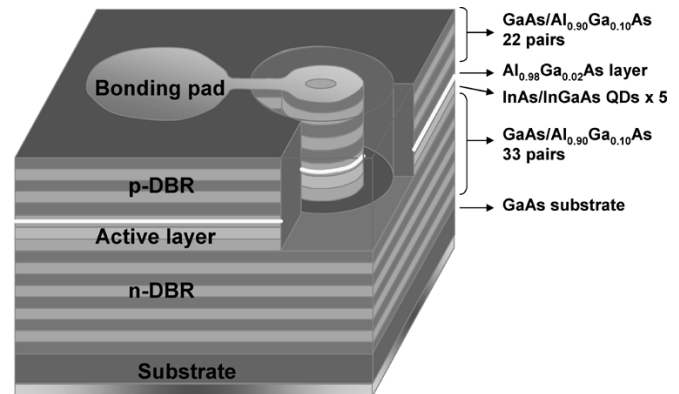


Fig. 1. QD VCSELs device structure.

mode suppression ratio (SMSR $>30 \text{ dB}$) and report the temperature performance and dynamic properties including bandwidth and eye diagram.

II. EXPERIMENT

All structures were grown on GaAs (100) substrates using molecular beam epitaxy by NL Nanosemiconductor GmbH (Germany). The epitaxial structure was as follows (from bottom to top): n^+ -GaAs buffer, 33.5-pair n^+ - $\text{Al}_{0.9}\text{Ga}_{0.1}\text{As}/n^+$ -GaAs (Si-doped) DBR, undoped active region, $p\text{-Al}_{0.98}\text{Ga}_{0.02}\text{As}$ oxidation layer, 22-pair p^+ - $\text{Al}_{0.9}\text{Ga}_{0.1}\text{As}/p^+$ -GaAs DBR (carbon-doped), and p^+ -GaAs (carbon-doped) contact layer. The graded-index separate confinement heterostructure active region consisted mainly of five groups of the QD's active region embedded between two linear-graded $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($x = 0$ to 0.9 and $x = 0.9$ to 0) confinement layers. The thickness of the cavity active region was 3λ . Each group of QDs was composed of three QD layers and was situated around the antinode of a standing wave. The wavelength detuning is about 12 nm with gain peak at 1266 nm , and cavity resonance at 1278 nm . Carbon was used as the p-type dopant in the DBR to increase the carrier concentration ($2 - 3 \times 10^{18} \text{ cm}^{-3}$). The interfaces of both the p-type and n-type $\text{Al}_{0.9}\text{Ga}_{0.1}\text{As}$ –GaAs DBR layers are linearly graded to reduce the series resistance. The optical characteristics of QDs were optimized through PL measurement and structural analysis. The details of the process were fully described in our previous work [11]. The mesa diameter of the fabricated device is $26 \mu\text{m}$ with a $5 \sim 6 \mu\text{m}$ oxide aperture, and the device surface is quasi-planar so that the annular p-contact metal and the bond pad are on the same level. The device structure is shown in Fig. 1. The p-contact was formed by directly depositing Ti–Pt–Au on the upper

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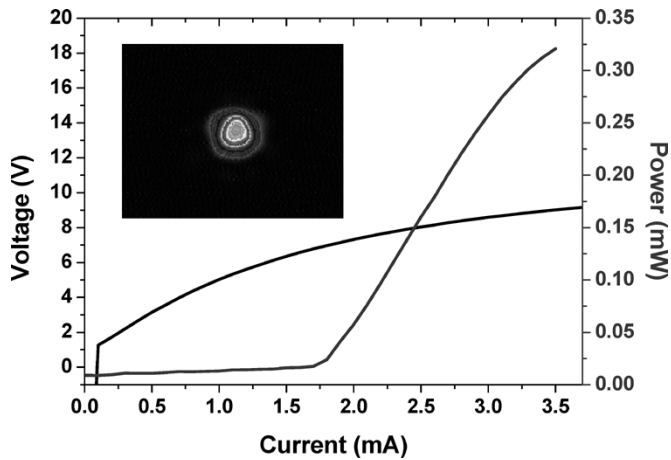


Fig. 2. L - I - V relationship of QD VCSEL. Inset is the near-field pattern of the VCSELs at 3 mA.

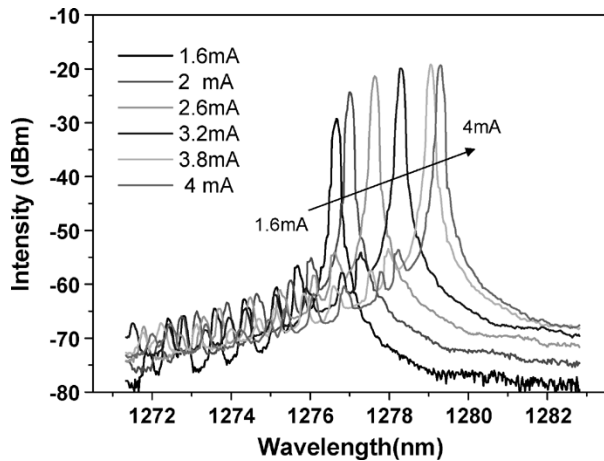


Fig. 3. Emission spectra of QD VCSEL at room temperature.

heavily doped p^+ GaAs contact layer, and Au-Ge-Ni-Au was deposited on the bottom side of the substrate after it had been thinned down. After metal annealing, the sample was immediately probe tested on the wafer level to extract the static operation characteristics.

III. RESULTS AND DISCUSSION

The dc characteristics of completed VCSELs were measured using a probe station, an Agilent 4145A semiconductor parameter analyzer, and an InGaAs photodiode. The spectra of the QD VCSELs were measured using an Advantest Q8381A optical spectrum analyzer. Fig. 2 plots curves of light output and voltage versus current (L - I - V). The threshold current is ~ 1.8 mA and the threshold current density is 7.6 kA/cm². The output power rollover occurs as the current increases above 4 mA with maximum optical output of 0.33 mW at 20 °C. In addition, the VCSEL is a single polarization in the full operating range with a ratio of ~ 20 dB. The inset of Fig. 2 is the near-field pattern of the QD VCSEL biased at 3 mA which indicated the fundamental mode lasing. Fig. 3 shows the typical emission spectra of the QD VCSELs, which indicate single transverse mode operation in the whole operation range with a lasing wavelength of ~ 1.278 μ m and SMSR > 30 dB.

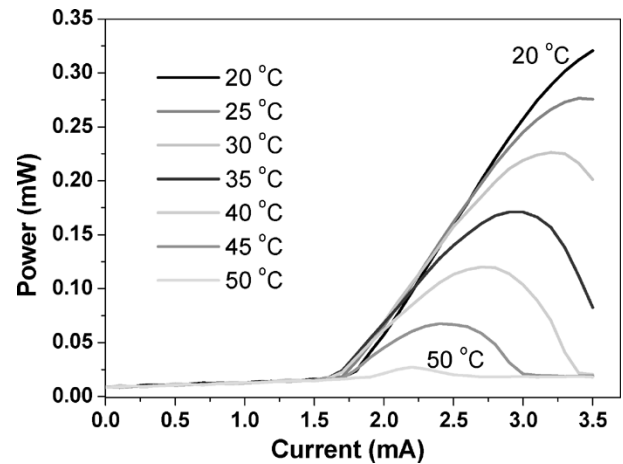


Fig. 4. Temperature-dependent L - I relationship of QD VCSEL.

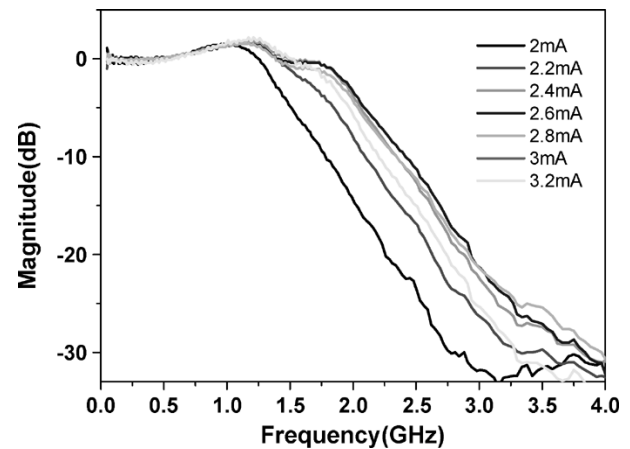


Fig. 5. Small signal modulation response of QD VCSEL.

To investigate the temperature dependence of the QD VCSEL, L - I curves were measured from room temperature to 55 °C with current step of 0.01 mA, as shown in Fig. 4. The threshold current varies only 0.15 mA ($< 10\%$ of I_{th}) with temperatures from 10 °C to 45 °C and the slope efficiency drops from 0.18 to 0.1 W/A. The small temperature dependence of threshold current corresponds to a characteristic temperature (T_0) of 450 K, a high value comparing with the InGaAs(N) VCSEL in 1.3 μ m. The high characteristic temperature was attributed to the wide gain spectra of the QD's gain media. When temperature increases, the wide gain spectra make the alignment between gain spectra and cavity resonance not sensitive, and therefore, improves the T_0 . However, increase of threshold current with temperature and the quench of output power were also observed after 50 °C which implies the gain of the QDs decreases severely in higher temperature.

The small signal response of VCSELs as a function of bias current was measured at 25 °C using a calibrated vector network analyzer (Agilent 8720ES) with wafer probing and a 50- μ m multimode optical fiber connected to a New Focus 25-GHz photodetector. Fig. 5 indicates that the modulation frequency increases with the bias current low current range. At a bias current of larger than 2.5 mA, the bandwidth saturate and the maximum 3-dB modulation frequency response is measured as ~ 2 GHz. In Fig. 6, the 3-dB bandwidth ($f_{3\text{ dB}}$) is plotted as a function of

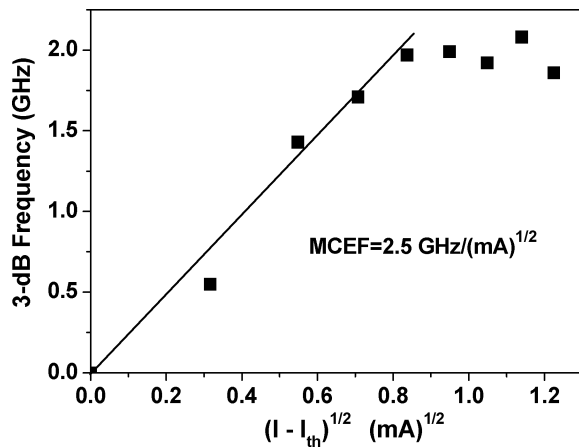


Fig. 6. The 3-dB frequency as a function of square root of current above threshold current.

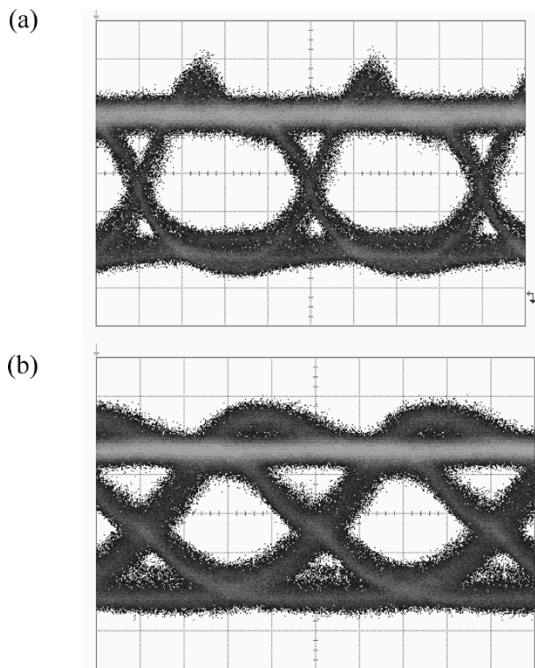


Fig. 7. Eye diagram of the QD VCSELs at (a) 1.25 and (b) 2.5 Gb/s (time scale was 200 and 100 ps/div).

the bias current. At low bias currents, the bandwidth increases in proportion to the square root of the current as expected from the rate equation analysis. The saturation of bandwidth was clearly observed as bias current increases above 2.5 mA which might be attributed to heating effect. Carrier depopulation in QDs subjected to the heated active region may suppress the material gain and put the intrinsic limit of high speed modulation. The modulation current efficiency factor is $\sim 2.5 \text{ GHz}/(\text{mA})^{1/2}$. Improvement can be done by increasing the QD's stacks and reducing the current density of each dot simultaneously. Finally,

we illustrate the eye diagram at 1.25 and 2.5 Gb/s (Fig. 7). The QD VCSEL shows a clear and symmetrical eye diagram at 1.25 Gb/s. At 2.5 Gb/s, the eye was degraded due to the overshoot and insufficient bandwidth. Future work will focus on enhancement of high-speed performance by reducing the device parasitic and thermal impedance.

IV. CONCLUSION

We present monolithic QD VCSELs operating in the 1.3- μm optical communication wavelength. The QD VCSELs have adapted fully doped structure on GaAs substrate. The output power is $\sim 330 \mu\text{W}$ with slope efficiency of 0.18 W/A at room temperature. Single-mode operation was obtained with SMSR of $>30 \text{ dB}$.

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