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# AN IMPROVED APPROACH FOR SMALL-SIGNAL EQUIVALENT-CIRCUIT PARAMETER DETERMINATION OF InGaP/GaAs HBT

Han-Yu Chen, 1 Kun-Ming Chen, 2 Guo-Wei Huang, 2 Chun-Yen Chang, 1 and Tiao-Yuan Huang 1

<sup>1</sup> Institute of Electronics
 National Chiao-Tung University
 Hsinchu, 300 Taiwan
 <sup>2</sup> National Nano Device Laboratories
 1001-1 Ta Hsueh RD.
 Hsinchu 300, Taiwan

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ABSTRACT: In this paper, an improved method for extracting the small-signal equivalent-circuit elements of an HBT is proposed. A more general HBT equivalent circuit and a more general explicit equation on the total extrinsic elements are introduced. Linear least-square algorithms are cleverly adopted to reduce the number of error function. As a result, the modified approach can yield a better fit between the measured and simulated S-parameters over the conventional method. © 2005 Wiley Periodicals, Inc. Microwave Opt Technol Lett 44: 456–460, 2005; Published online in Wiley InterScience (www.interscience.wiley.com). DOI 10.1002/mop.20666

**Key words:** *HBT*; device modeling; small-signal modeling; parameter extraction; microwave transistor

## 1. INTRODUCTION

In recent years, much effort has been devoted to analytical approaches of HBT equivalent-circuit parameter extraction [1-6]. Many closed-form representations of intrinsic circuit elements have been derived for direct extraction of equivalent-circuit elements [3-6]. However, direct-extraction techniques are sensitive to the accuracy of extracted extrinsic parameters. At microwave frequencies, it has been noted that a slight change in any of the extrinsic elements' value could result in drastic changes in the intrinsic elements' value [7, 8]. Therefore, a strong correlation exists between the extrinsic and intrinsic circuit elements. Based on this correlation, Shirakawa et al. [7] and Ooi et al. [8] proposed a technique to determine the FET equivalent-circuit elements, which does not require additional measurements other than the S-parameters. Recently, the technique has been extended to HBT equivalent-circuit modeling [4, 5]. To reduce the optimization variables, the assumption that the imaginary part of  $Z_{12}$  of an HBT tends toward zero is adopted to establish the relation between extrinsic inductance and other circuit elements. Although the reduction of optimization variables greatly saves computer time, it may cause the modeling routine to be trapped in a local minimum, due possibly to a nongeneral assumption, since the imaginary part of  $Z_{12}$  of an HBT shows a frequency response in most situations.

In this paper, a more general HBT equivalent circuit, which separates the base/emitter delay from base/emitter RC circuit and takes into consideration the finite resistance between the base and collector, is used to improve the methods in [4, 5]. A more general analytic equation, which establishes the relationship between extrinsic inductance and other circuit elements, is introduced to avoid the abovementioned problem. In addition, linear least-square algorithms are cleverly adopted to reduce the number of error function, and only six intrinsic circuit elements are involved during optimization.

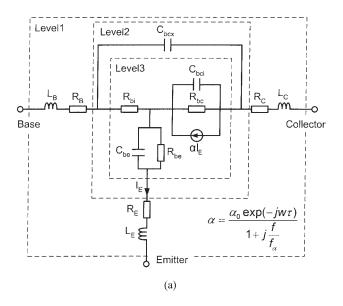
#### 2. EQUIVALENT CIRCUIT OF THE HBT TRANSISTOR

As shown in Figure 1(a), a general equivalent circuit for InGaP/GaAs HBT is used in this study. As indicated in this figure, the equivalent circuit is divided into three parts. The Level-2 part excludes the elements  $R_E$ ,  $L_E$ ,  $R_B$ ,  $L_B$ ,  $R_C$ , and  $L_C$  from Level 1, while the part of Level 3 excludes the element  $C_{bcx}$  from Level 2. In Figure 1(b), the equivalent circuit of Level 2 in the common-collector configuration is shown. The respective ABCD parameters,  $A_{Level2}$ , are described as follows:

$$A_{Level2} = \begin{bmatrix} A_{11,Level2} & A_{12,Level2} \\ A_{21,Level2} & A_{22,Level2} \end{bmatrix}, \tag{1}$$

where

$$A_{11,Level2} = 1 + R_{bi}Y_{bc}, (2a)$$



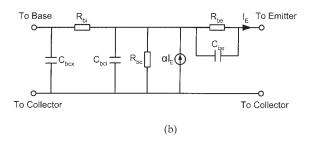


Figure 1 Equivalent circuit of (a) an InGaP/GaAs HBT and (b) Level 2 in the common collector configuration

$$A_{12,Level2} = R_{bi}(1-\alpha) + \frac{1}{Y_{be}}(1+R_{bi}Y_{bc}),$$
 (2b)

$$A_{21,Level2} = Y_{bc} + Y_{ex}(1 + R_{bi}Y_{bc}),$$
 (2c)

$$A_{22,Level2} = (1 - \alpha)(1 + R_{bi}Y_{ex}) + \frac{1}{Y_{be}}[Y_{bc} + Y_{ex}(1 + R_{bi}Y_{bc})],$$
(2d)

with

$$Y_{bc} = \frac{1}{R_{bci}} + j\omega C_{bci}, \tag{3a}$$

$$Y_{be} = \frac{1}{R_{be}} + j\omega C_{be}, \tag{3b}$$

$$Y_{ex} = j\omega C_{bcx}. (3c)$$

The transformation between the Y-parameters of the common-emitter configuration  $(Y_e)$  and the Y-parameters of the common-collector configuration  $(Y_c)$  are given by

$$Y_{c} = \begin{bmatrix} Y_{11,e} & -(Y_{11,e} + Y_{12,e}) \\ -(Y_{11,e} + Y_{21,e}) & (Y_{11,e} + Y_{12,e} + Y_{21,e} + Y_{22,e}) \end{bmatrix}.$$
(4)

The extrinsic part of the HBT, which is located outside Level 2, is related to Level 2 through the following expressions:

$$Z_{Level2} = Z_{Level1} - Z_{ext}, (5)$$

$$Z_{ext} = \begin{bmatrix} R_E + R_B + j\omega(L_E + L_B) & R_E + j\omega(L_E) \\ R_E + j\omega(L_E) & R_E + R_C + j\omega(L_E + L_C) \end{bmatrix},$$
(6

where  $Z_{Level1}$  and  $Z_{ext}$  are the overall and extrinsic Z-parameters, respectively.

#### 3. ANALYTICAL DETERMINATION OF THE EQUIVALENT-CIRCUIT ELEMENTS

Similar to conventional FET modeling, once the extrinsic elements are known, the intrinsic elements can be determined analytically. The overall measured S-parameters are first converted to Z-parameters, and by using Eqs. (5) and (6), the Z-parameters in the common-emitter configuration of Level 2 are obtained. By using Eq. (4) and standard network-parameter transformation, the ABCD parameters in common-collector configuration of Level 2,  $A_{level2}$ , are obtained. Based on the equivalent circuit shown in Figure 1(b), the equivalent-circuit elements in Level 2 are derived as follows:

$$R_{bc} = \frac{1}{\text{Re}(Y_{bc})},\tag{7a}$$

$$R_{be} = \frac{1}{\text{Re}(Y_{be})},\tag{7b}$$

$$C_{bci} = \frac{\text{Im}(Y_{bc})}{\omega},\tag{7c}$$

$$C_{be} = \frac{\operatorname{Im}(Y_{be})}{\omega},\tag{7d}$$

$$C_{bcx} = \frac{\text{Im}(Y_{ex})}{\omega},\tag{7e}$$

$$R_{bi} = \frac{\text{Re}[(A_{11,level2} - 1)A_{11,level2}^*]}{\text{Re}[A_{21,level2}A_{11,level2}^*]},$$
(7f)

$$\alpha = 1 - (A_{11,Level2} A_{22,Level2} - A_{12,Level2} A_{21,Level2}), \tag{7g}$$

with

$$Y_{ex} = \frac{-\text{Re}[(A_{11,level2} - 1)A_{21,level2}^*]}{\text{Re}[(A_{11,level2} - 1)A_{11,level2}^*]},$$
 (8a)

$$Y_{bc} = \frac{(A_{11,level2} - 1)}{R_{b2}},$$
 (8b)

$$Y_{be} = \frac{Y_{bc}}{(A_{22,level2} - Y_{ex}A_{12,level2} + \alpha - 1)}.$$
 (8c)

The three parameters of  $\alpha_0$ ,  $\tau$ , and  $f_\alpha$ , are determined directly from the frequency dependence of Eq. (7g) with linear least-square algorithms. The parameters  $\alpha_0$  and  $f_\alpha$  are first extracted from  $|\alpha|^2$ , which leads to the linear least-squares problem:

$$\min \sum_{n=1}^{N} \left[ |\alpha(\omega_n)|^2 + \frac{f_n^2 |\alpha(\omega_n)|^2}{f_\alpha^2} - \alpha_0^2 \right]^2, \tag{9}$$

where w and N are the angular frequency and the number of measured frequencies, respectively. The transit time  $\tau$  is then calculated using the mean value of

$$\tau = -\frac{1}{\omega} \left[ \angle \alpha + \tan^{-1} \left( \frac{f}{f_{\alpha}} \right) \right]. \tag{10}$$

The advantage of using linear least-squares algorithms is that the parameters  $\alpha_0$ ,  $\tau$ , and  $f_{\alpha}$  can be directly solved by linear matrix calculation, thus greatly reducing the number of error functions used for determining the extrinsic circuit elements.

As shown in Figure 1(a), since the imaginary part of  $Z_{11,Level3} - Z_{12,Level3}$  is zero [6], a general analytic equation in terms of the equivalent-circuit elements can be found, which is reproduced here as

$$\omega L_{B} + \frac{\omega C_{bcx} C_{bci} R_{bi} R_{B}}{C_{bcx} + C_{bci}} - \frac{\omega C_{bcx} C_{bci} R_{bi}}{C_{bcx} + C_{bci}} \operatorname{Re}(Z_{11,Level1} - Z_{12,Level1}) - \operatorname{Im}(Z_{11,Level1} - Z_{12,Level1}) = 0.$$
(11)

From Eq. (11), the extrinsic elements,  $L_B$  can be expressed in terms of the other extrinsic elements as follows:

$$L_B = f_0(w_i, L_C, L_E, R_B, R_C, R_E) = f_0(\omega_i, Z_{ext} - L_B).$$
 (12)

The representation  $Z_{ext} - L_B$  denotes all the extrinsic elements except the base inductance. So, once the values of  $R_B$ ,  $R_C$ ,  $R_E$ ,  $L_C$ , and  $L_E$  are known,  $L_B$  can be determined. By substituting Eq. (12) into Eq. (7), the intrinsic elements can be expressed in a similar way as follows:

$$R_{bi} = f_1(\omega_i, Z_{ext} - L_B), \tag{13a}$$

$$C_{bcx} = f_2(\omega_i, Z_{ext} - L_B), \tag{13b}$$

$$R_{be} = f_3(\omega_i, Z_{ext} - L_B), \tag{13c}$$

$$C_{be} = f_4(\omega_i, Z_{ext} - L_B), \tag{13d}$$

$$R_{bc} = f_5(\omega_i, Z_{ext} - L_B), \tag{13e}$$

$$C_{bci} = f_6(\omega_i, Z_{ext} - L_B). \tag{13f}$$

If the extracted equivalent circuit elements are valid at every measured frequency, the elements' values will depict negligible frequency response. By finding the specific extrinsic elements  $R_B$ ,  $R_C$ ,  $R_E$ ,  $L_C$ , and  $L_E$ , which minimize the frequency dependence of the intrinsic elements, the equivalent-circuit modeling is reduced to an optimization problem of finding the specific five extrinsic elements.

#### 4. THE EXTRACTION ALGORITHM AND RESULTS

In our proposed method, the first objective function is to optimize the extrinsic elements so that the intrinsic elements exhibit less frequency dependence; this is expressed as

$$E_{1}^{k}(R_{B}, R_{C}, R_{E}, L_{C}, L_{E}) = \frac{1}{N-1}$$

$$\times \sum_{i=0}^{N-1} \left| \rho_{i} f_{k}(\omega_{i}, Z_{ext} - L_{B}) - \sum_{j=0}^{N-1} \rho_{i} f_{k}(\omega_{j}, Z_{ext} - L_{B}) \right|^{2}, \quad (14)$$

where k varies from 1 to 6, the over bar indicates the mean values, and  $\rho_k$  is a normalizing factor to make the values of  $f_k$  vary between zero and one.

For stable calculation of the *S*-parameters, the second objective function, Eq. (15), is considered as a loose constraint:

$$E_{2}^{k}(R_{B}, R_{C}, R_{E}, L_{C}, L_{E}) = \sum_{p=1}^{2} \sum_{q=1}^{2} \sum_{i=0}^{N-1} \times W_{pq} |S_{pq}^{c}(w_{i}, Z_{ext} - L_{B}) - S_{pq}^{m}(\omega_{i}, Z_{ext} - L_{B})|^{2}, \quad (15)$$

where the superscripts c and m denote the calculated and measured S-parameters, respectively, and  $W_{pq}$  is the weighting factor of  $S_{pq}$ . The mean values of intrinsic elements are used for computing  $S_{pq}^{C}$  and  $L_{B}$ . The extended error vector is then composed of

$$\varepsilon(\omega_{i}, R_{B}, R_{C}, R_{E}, L_{C}, L_{E}) = \begin{bmatrix} \sum_{k=1}^{6} E_{1}^{k}(R_{B}, R_{C}, R_{E}, L_{C}, L_{E}) \\ E_{2}^{k}(R_{B}, R_{C}, R_{E}, L_{C}, L_{E}) \end{bmatrix}.$$
(16)

As indicated initially in Figure 2, the values of extrinsic parameters  $R_B$ ,  $R_C$ ,  $R_E$ ,  $L_C$ , and  $L_E$  are selectively assigned according to their appropriate range. Hence, the extrinsic parameters are close to the physically meaningful minimum of the optimization error function. Then, the extrinsic inductance  $L_B$  and all the parameters within the Level-2 part are evaluated from Eqs. (11) and (7). The process is conducted iteratively in order to make Eq. (16) minimum. If the error vector of Eq. (16) is below the designed error criteria, the extrinsic and intrinsic elements will be extracted and thus the equivalent-circuit modeling is completed.

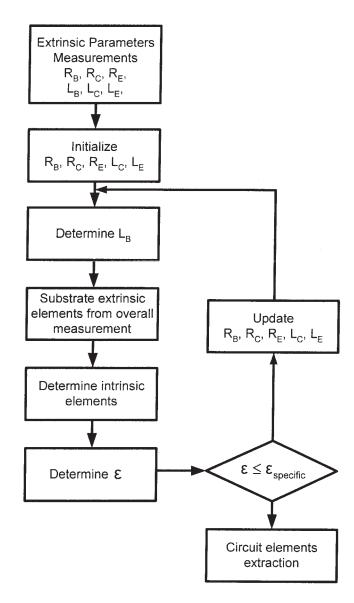


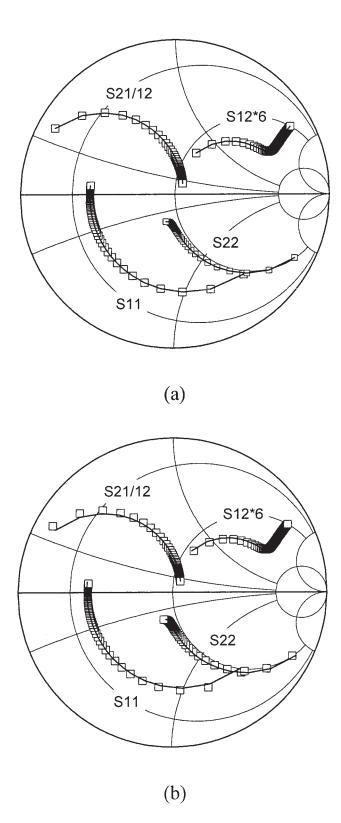
Figure 2 Parameter extraction algorithm

To validate the accuracy of the proposed extraction technique, several InGaP/GaAs HBTs fabricated in house [9] were investigated. The flowchart shown in Figure 2 was implemented on Agilent IC-CAP EESoft.

Figure 3 shows a comparison between the measured and calculated S-parameters, for both our proposed method and the conventional method [5]. As shown in Figure 3, both methods yield good agreement over the entire frequency range. For ease of comparison, no parameter reduction was used in the conventional equivalent circuit extraction method. Table 1 gives a comparison of the modeling results and the residual error, quantifying the accuracy for both the conventional method and proposed method according to

$$\|\varepsilon\| = \frac{1}{4N} \sum_{p=1}^{2} \sum_{q=1}^{2} \sum_{i=0}^{N-1} \frac{\left| S_{pq}^{c}(\omega_{i}) - S_{pq}^{m}(\omega_{i}) \right|}{\left| \max(S_{pq}^{m}(\omega_{i})) \right|}.$$
 (17)

A smaller residual error is observed in the proposed method. The reason for this may be that more complete intrinsic circuit elements are used in this study (nine in the proposed method, seven in the conventional method). Also, fewer error functions (seven in the proposed method, eight in the conventional method) and fewer optimization variables (five in the proposed method, six in the conventional method) are involved in the proposed method, and thus greatly reduce the complexity of the optimization routine and



**Figure 3** Simulated (—) and measured ( $\square$ ) S-parameters from 1 to 25 GHz biased at  $V_c=1.5$  V and  $I_c=40$  mA for the (a) new and (b) conventional methods

TABLE 1 Modeling Results and Related Residual Data-Fitting Errors for our Proposed Method and Conventional Method

$V_c = 1.5 \text{ V}$ $I_b = 600 \mu\text{A}$ $I_c = 40 \text{ mA}$	This Study	Ref. [5]
$L_B$ (pH)	26.5	29.07
$L_C$ (pH)	12.7	10.86
$L_E$ (pH)	12.3	19.96
$R_B(\Omega)$	5.75	6.739
$R_C(\Omega)$	6.1	5.666
$R_E(\Omega)$	5.72	4.03
$C_{bcx}$ (fF)	79.01	77.12
$C_{bci}$ (fF)	27.79	28.76
$C_{be}$ (fF)	1089	1162
$R_{bi}(\Omega)$	6.276	2.915
$R_{be}(\Omega)$	0.998	2.856
$R_{bc}$ (k $\Omega$ )	54.88	_
$\alpha_0$	0.978	0.974
$\tau$ (ps)	1.972	3.801
$f_{\alpha}$ (GHz)	87.89	_
Residual error	0.27%	3.97%

increase the possibility of escaping from the local minimum during the optimization procedure.

#### 5. CONCLUSION

An improved and more efficient technique for small-signal HBT equivalent circuit parameter extraction has been introduced. Comparing with the conventional method, the closed-form representation of a more general T-topology circuit with nine intrinsic circuit elements has been presented. The transistor parameters  $\alpha_0$ ,  $\tau$ , and  $f_{\alpha}$  are cleverly derived with least-square algorithms, which reduce the number of error functions used to determine the extrinsic elements. Also, a more general explicit equation for the total extrinsic elements has been derived, resulting in a reduction of the number of optimization variables. As a result, a better fit between the measured and simulated S-parameters is achieved, as compared to the conventional method.

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# A THREE-DIMENSIONAL PLANAR PHOTONIC CRYSTAL USING CONDUCTING POLYMERS

L. Oyhenart, V. Vignéras, F. Demontoux, and J. P. Parneix Laboratoire de Physique des Interactions Ondes-Matière (PIOM) UMR CNRS 5501 ENSCPB, 16 Avenue Pey-Berland 33607 Pessac, France

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ABSTRACT: In this paper, we study the possibility of replacing metal by conducting polymer in the formation of 3D metallic photonic crystals. Subject to obtaining the highest conductivity, it is possible to obtain similar comportment between metallic and polyaniline structures. Aspects of technological interest, such as reduction of cost and the weight of the structure, are discussed. © 2005 Wiley Periodicals, Inc. Microwave Opt Technol Lett 44: 460–463, 2005; Published online in Wiley InterScience (www.interscience.wiley.com). DOI 10.1002/mop. 20667

**Key words:** photonic crystal; photonic band gap; conducting polymer; polyaniline; technology

#### 1. INTRODUCTION

Throughout the past decade, there has been a great deal of interest in photonic crystals [1], which are periodic dielectric materials capable of inhibiting the propagation of light over the spectral region known as the photonic band gap (PBG). Thus numerous applications of photonic crystals have been proposed for improving the performance of devices with operating frequencies ranging from the microwave to optical, including zero-threshold lasers, novel resonators and cavities, and efficient microwave antennas [2, 3]. Although the use of photonic crystals made of dielectric materials has been successful in various applications, some properties restrict usage. Metals exhibit high absorption at optical frequencies; they act like nearly perfect conductors at lower microwave and millimeter-wave frequencies that minimize the problems related to absorption. A metallic structure can made at a substantially reduced size and cost, compared to a dielectric one [4]. In order to reduce the cost and weight further, new conducting materials can replace the metal used in the metallic photonic crystal.

The discovery of high conductivity in doped polyacetylene in 1977 [5] attracted considerable interest in the application of polymers as semiconducting and conducting materials, and allowed three scientists to receive the Nobel Prize in Chemistry in 2000 [6]. Since then, many researchers have been working on the mechanism of polymer conduction and polymer microelectronic devices.

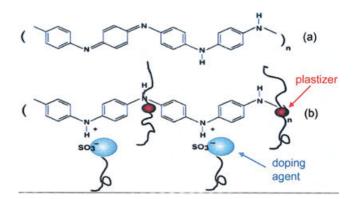
These devices are potentially useful in a number of applications where high speeds are not essential, for example, low-cost memory devices, gas sensors, microwave antenna, PLED display, and so on

The object of our study is to realize PBG structures using conducting polymers in the microwave-frequency range. The first step is the electric characterization of the conducting polymer to optimize the choice of the PBG structure. Using a numerical analysis of various 3D structures, we can show that if the conductivity of polymer is higher than 1200 S/m, it is possible to realize "similar metallic" photonic crystal. Such structures have been realized and compared to metallic ones. From a technological point of view, various methods of low-cost impression can be adapted to conducting polymers.

#### 2. CONDUCTING POLYMERS

Polymers are known for their insulating properties. The structure of polymer is a cluster of relatively disordered chains. A polymer becomes conducting if it is conjugated and doped. A conjugated polymer presents an alternation of simple and double bonds on its principal chain. Delocalized  $\pi$  electrons are very reactive and can participate to oxido-reductions reactions. Doping consists of oxidizing or reducing the principal chain; various techniques can be used, such as chemical, electro-chemical, or physical (ionic implantation) methods. The charge carriers are free to move along the chain. This type of structure is called conducting polymer. Its conductivity is controlled by the density of the charge carrier and its associated mobility. The charge transport is made up of two components: intra-chain transport and inter-chain transport. The first component is one-dimensional, while the second three-dimensional. Inter-chain transport is made by successive jump of chain; and it is disordered.

The chosen conducting polymer is polyaniline (PANI), as shown in Figure 1. The polyaniline used is chemically doped from the emeraldine base form. This process allows us to obtain a high level of conductivity. The company PANIPLAST provides it in solution form in dichloroacetic acid and formic acid. Like the majority of conducting polymers, polyaniline is not soluble; thus, we can talk about dispersion instead of a setting in solution. According to the quantities of various solvents used and the viscosity sought, conductivity can vary by one order of magnitude. Conductivity also depends on the process of evaporation of solvents; it can be selected on a range from 10 to 6000 S/m [7]. After drying the solution, we obtain a thin supple film of controllable



**Figure 1** Polyaniline molecule: (a) polyaniline (emeraldine base and insulating form); (b) doped polyaniline (conductive form). [Color figure can be viewed in the online issue, which is available at www.interscience.wiley.com.]