

Efficiency improvement of organic bifunctional devices by applying omnidirectional antireflection nanopillars

Cite this: *RSC Adv.*, 2014, 4, 9588Yu-Hsuan Ho,^{ab} Hsun Liang,^c Shun-Wei Liu,^d Wei-Cheng Tian,^b Fang-Chung Chen^c and Pei-Kuen Wei^{*a}

OLED/OPV tandem devices have attracted great interest due to their various functions in green technology and potential sensing applications. Here we report a novel concept to improve the luminous efficiency and energy conversion efficiency of tandem bifunctional devices by integrating antireflection structures consisting of irregular nanopillars as the capping layer. The nanopillars were made on flexible polycarbonate substrates by using a nanoimprint method with an anodized aluminum oxide template. The average reflectivity of the patterned polycarbonate substrate at visible wavelengths was reduced to 3.3%. With the antireflection structures, the luminous intensity for bifunctional devices was improved by 41.9% compared to a device with a flat capping layer. The power conversion efficiency was improved from 0.48% to 0.61%, a 27.0% enhancement at normal incidence.

Received 22nd November 2013
Accepted 2nd December 2013

DOI: 10.1039/c3ra46936k

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Introduction

Organic electronics, such as organic light-emitting diodes (OLEDs) and organic photovoltaic (OPV) cells have drawn a lot of attention due to their merits of being feasible for large area production, and for ultrathin and flexible devices.^{1–4} Due to the wide variety of device substrates available, organic electronics provide advantages for the use of low-cost, flexible, and roll-to-roll fabrication processes, and top light-emitting and harvesting organic optoelectronic devices, transmitting light through the top electrode. Recently, OLED/OPV tandem devices have attracted great interest due to their potential uses in display and sensing applications and use for stand-alone operation isolated from electrical power sources.^{5–8} For displays, the stacked OPV structure can serve as the light absorber and effectively reduce the ambient reflection from the metal cathode to achieve a high contrast ratio and energy recycling efficiency. Optical sensors are another important application for this kind of bifunctional device. The OLED component acts as the active light source in scanners and the stacked photovoltaic component serves as the photodetector, and the absorption spectrum of the photovoltaic component matches well with the luminous spectrum of the stacked OLED.^{9,10} These tandem devices provide significant

benefits as well as superior functionality for applications in low cost sensing devices and compact information systems.

However, the strong absorption of the stacked OPV structure also decreases the luminous efficiency of tandem OLED devices. It is often a trade-off between the high contrast ratio and the external quantum efficiency. In order to compensate for the loss of photons absorbed by the OPV structure, micro- or nano-structures, such as scattering layers, microlens arrays, and photonic crystals have been applied to extract the light confined in the substrate to increase the OLED luminescence.^{11–16} But the scattering due to surface roughness, which occurs when applying micro- or nanostructures, may influence the transmittance of the incident light and decrease the OPV efficiency.

In order to improve this kind of bifunctional device, an antireflection (AR) technique is a suitable approach to improve the luminous and energy conversion efficiencies simultaneously.^{17–21} The use of single-layer or multilayer AR coatings between the device substrate and air can utilize the destructive interference between the light reflected from the interfaces and effectively reduce surface reflection. However, the AR characteristics obtained with these coating methods can only be observed for a limited spectral range and optimized to a specific viewing angle. These drawbacks of the conventional coating methods can be sufficiently solved by graded index AR techniques.²² Compared with conventional coating methods, the graded-index AR methods were theoretically predicted to have improved broadband AR behavior by up to one order of magnitude and an omnidirectional AR effect. There are two approaches to make graded index AR layers. One is to coat multi-films with layers with a gradually changing refractive index, and the other is to use a tapered morphology.^{23–26} For

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tapered morphology, the porosity of a layer should be graded in a controlled manner and has a characteristic dimension which is much smaller than the wavelengths of incident light.

In this work, we used the thermal nanoimprint method to fabricate a tapered morphology with sub-wavelength nanopillars and attached it on the top electrode of a bifunctional device. The bifunctional device was made by integrating an organic light-emitting diode on top of a transparent organic solar cell. The nanoimprinted AR nanopillars formed a graded index layer between the polycarbonate (PC, $n = 1.57$) and air ($n = 1$). This antireflection layer not only improved the luminous efficiency of the OLED by diminishing the total internal reflection in the substrate, but also increased the light incidence to enhance the power conversion efficiency of the stacked OPV structure. Simultaneously, the contrast ratio also showed great improvement due to both the higher luminous intensity and the lower ambient reflection.

Experimental

As depicted in Fig. 1(a), we used a semi-transparent metal layer (Al 5 nm/Ag 15 nm) as the connecting layer to cascade the organic solar cell with the conventional tris(8-hydroxyquinolino)aluminum (Alq₃) based OLED. First, the pre-cleaned bottom glass substrate was evaporated with 100 nm-thick silver as the opaque anode. A self-assembled monolayer (SAM) of a binary mixture of *n*-decanethiol (HDT) and 3,3,4,4,5,5,6,6,7,7,8,8,9,9,10,10,10-heptafluorodecanethiol (FDT) with a mole ratio of HDT : FDT = 1 : 3 was stamped with a polydimethylsiloxane (PDMS) mold on the opaque silver electrode to modify its work function. After introducing the SAM layer, a subphthalocyanine (SubPc, 11 nm) thin film as the donor material, fullerene (C₆₀, 45 nm) as the acceptor material and bathocuproine (BCP, 7 nm) as the buffer layer were deposited sequentially. Then a connection layer of thin metal film was evaporated as the cathode of the solar cell as well as the anode of the OLED. An additional SAM layer was introduced between the Ag thin film and the cascaded OLED structure in order to modify the work function of Ag and improve the hole

injection ability. In the OLED fabrication process, the hole-transport layer (NPB, *N,N'*-di-[(1-naphthalenyl)-*N,N'*-diphenyl]-(1,1'-biphenyl)-4,4'-diamine) and the electron-transport layer (Alq₃, tris-(8-quinolinolato)aluminum) were evaporated sequentially on the connection layer. LiF and Al were set as the semi-transparent cathode of the device. The chamber pressure of the evaporation process was controlled under ultra-high vacuum (around 5×10^{-6} torr). After the device fabrication process, we evaporated an approximately 1 μ m-thick NPB film on both the organic device and the nanoimprinted PC film. The devices with antireflection structures were then merged together. Fig. 1(b) shows photos of the tandem device operated in OPV and OLED modes, respectively. We can see the photocurrent measured by a commercial volt-ohm-milliammeter under illumination by outdoor sunlight. When we increased the operation voltage, the green emission from the OLED was clear even under outdoor sunlight.

In the imprinting process, the prepared PC film is thermally-nanoimprinted by a nanoporous anodized aluminum oxide (AAO) template. The diameter of the nanoholes of AAO is about 150 nm. Its SEM image is shown in Fig. 2(a). The imprinted AR nanostructure was made on the bottom side of the PC film, which is about 200 μ m-thick. The PC film was heated in a steel chamber with both top and bottom hot plates at a temperature higher than the glass transition temperature of polycarbonate (about 170 $^{\circ}$ C). The heated PC film was imprinted by an AAO template to form the antireflection nanopillars. To have a uniform air pressure on the PC film, a thin polyethylene

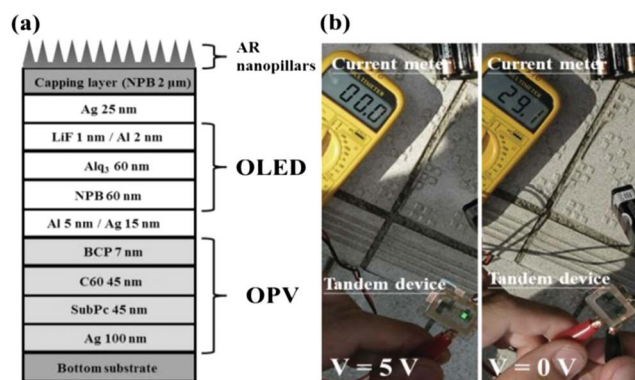


Fig. 1 (a) Structure of the OLED/OPV tandem device and (b) pictures of the semi-transparent tandem device under OPV (right figure) and OLED (left figure) operation.

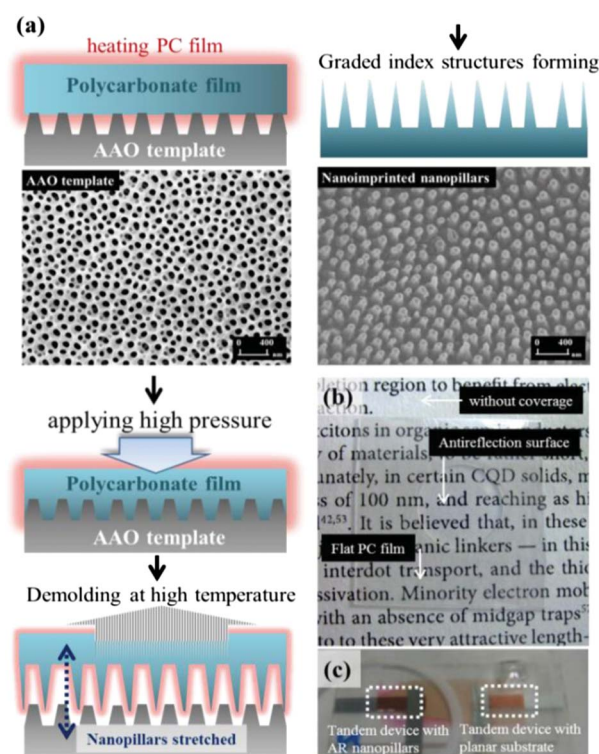


Fig. 2 (a) The thermal nanoimprint process with the AAO template. Pictures of (b) a paper and (c) the bifunctional devices covered with the patterned PC film under a bright light source.

terephthalate (PET) film was placed on the PC film and sealed by the steel chamber. The air pressure in the chamber was increased to 25 kg cm^{-2} so that the nanopores in the AAO were filled with molten polycarbonate. After five minutes of imprinting, the whole system was cooled down to a specified temperature (30, 50, 70, 100, 150 °C) and the air pressure was released. Then the AAO template was demolded immediately. Tapered nanopillars were formed on the PC film surface after the removal of the AAO template. The remaining aluminum layer was dissolved in a chromic acid solution for 3 hours. In general, applying a higher pressure and a longer imprint time increase the average height of the nanopillars and gives better AR effects. But these also make the AAO template tightly stick to the PC substrate and lead to damage of the transferred PC patterns during the demolding process. The optimized imprint pressure was $\sim 25 \text{ kg cm}^{-2}$ at a temperature of 170 °C. We also increased the demolding temperature to further stretch the nanopillars by the friction force of the demolding process. More tapered nanopillars were formed due to the viscous effect of the PC film near the melting temperature.

Fig. 2(b) and (c) show pictures of the imprinted films taken by a CCD camera under bright light illumination. The light source was a 60 W incandescent lamp set in front of the paper and the tandem devices. Obviously, a darker image was observed for the nanopillars due to the antireflection effect.

Fig. 3 shows AFM images of the nanopillars under different fabrication conditions. It can be seen that the nanopillars did not follow the cylindrical profiles of the AAO nanopores. They formed tapered shapes with an irregular distribution. As shown in Fig. 4(a), we found that the average height of the nanopillars was linearly proportional to the pressure applied. Moreover, we increased the demolding temperature and stretched the nanopillars using the friction force of the demolding process. A tapered surface morphology and higher nanopillars were found when the demolding temperature was higher. To check the antireflection properties of the tapered nanopillar structures, we measured the reflection spectra of PC films with and without nanostructures. Fig. 4(b) shows the average reflectivity of films

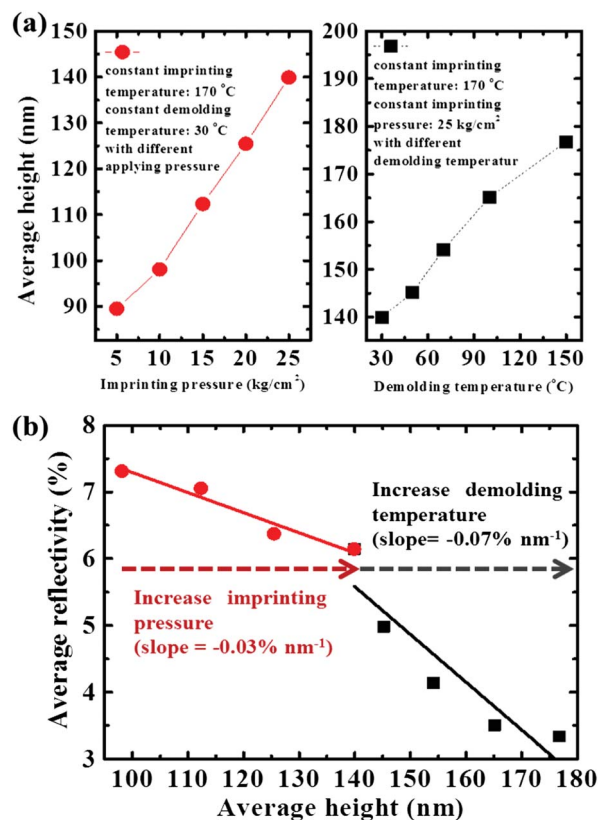


Fig. 4 (a) Average height of nanopillars under different imprinting pressures and demolding temperatures. (b) Average reflectivity in the visible spectrum (400 nm–800 nm) of different AR nanopillars.

with imprinted nanopillars with different average heights. The diameter of the nanopillars was 150 nm and the average height was determined by the imprinting pressure. The referenced light was incident on the sample in the normal direction, and the reflected light was measured by an integral sphere. The reflectance decreased with increasing average height. When we increased the demolding temperature to stretch the nanopillars, a more rapid decrease of average reflectance was found due to the more tapered surface morphology. Compared with the flat PC film, the average reflectivity of the patterned PC substrate was reduced from 10.2% to 3.3% in our most optimized case.

Results and discussion

Comparing the J - V characteristics of the tandem device and a reference top-emission OLED (TOLED) in Fig. 5(a), it is found that the current density of the tandem device is slightly lower than the TOLED. This is due to the increased series resistance from the tandem OPV structure. In addition, the current efficiency is lower due to the strong absorption of the OPV component, as shown in Fig. 5(b). The current efficiency decreased from 3.4 cd A^{-1} to 1.78 cd A^{-1} , which is about a 53.2% loss. After applying AR nanopillars on the tandem device to extract the light confined in the high-index organic layers, the luminance was improved by 43.3%. A picture of the tandem

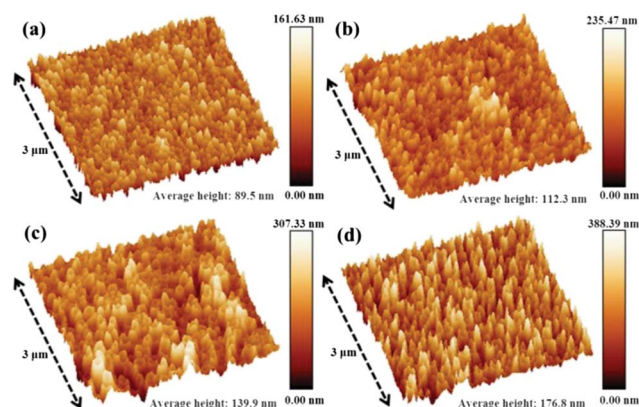


Fig. 3 Thermal nanoimprint results of PC films with an AAO template under different imprinting pressures and demolding conditions: (a) 5 kg cm^{-2} , (b) 15 kg cm^{-2} , (c) 25 kg cm^{-2} for room temperature demolding and (d) 25 kg cm^{-2} for high temperature demolding.

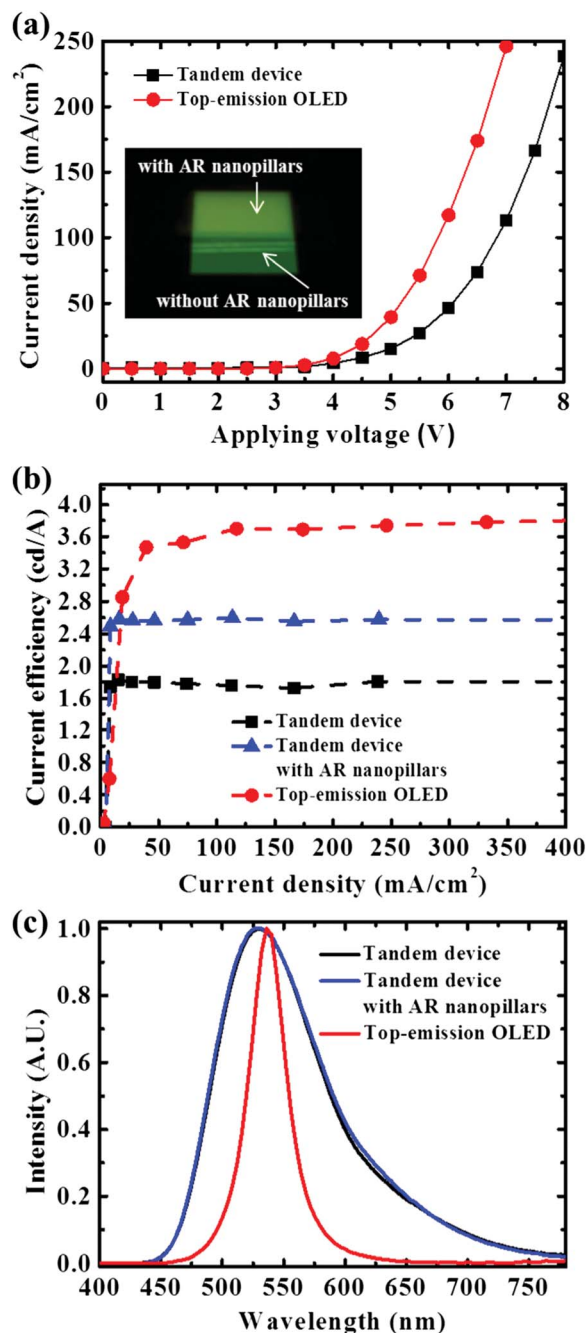
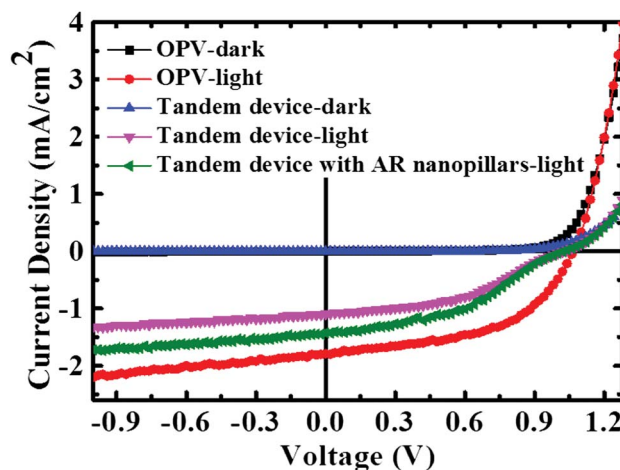


Fig. 5 (a) Current density vs. voltage characteristics (inset: an image of the tandem device pixel partially covered with AR nanopillars), (b) current efficiency vs. current density curves and (c) emission spectra of the TOLED and bifunctional devices (in electroluminescence-mode).

device partially covered with AR nanopillars is shown in the inset of Fig. 5(a), in which an obvious increase of the brightness can be observed. It is noted that the nanopillars have a broadband antireflection behavior. This indicates that the extraction enhancement is the same for different wavelengths. The emitting spectra would not be modified by these nanostructures. The peak wavelength was the same at 530 nm as shown in Fig. 5(c). There is no distortion in the color emitted.



Device	V _{OC} (V)	J _{SC} (mA/cm ²)	FF (%)	PCE (%)
OPV	1.07	1.79	51.58	0.99
Tandem	1.03	1.1	42.79	0.48
Tandem+AR	1.03	1.43	41.41	0.61

Fig. 6 Current density vs. voltage of the bifunctional devices with and without AR nanopillars and a reference organic solar cell, in darkness and under AM 1.5 illumination (under photovoltaic-mode operation).

Fig. 6 shows the J - V characteristics of the bifunctional device and a reference organic solar cell operated in the OPV mode under dark and light illumination conditions. The short circuit current density (J_{SC}), open circuit voltage (V_{OC}), fill factor (FF), and power conversion efficiency (PCE) of the bifunctional device and the reference OPV device are shown in Fig. 6. Under AM 1.5G 1-sun solar illumination, the bifunctional device gives the following performance for: short circuit current density: 1.1 mA cm⁻², open circuit voltage: 1.03 V, fill factor: 42.79%, and has a power conversion efficiency of 0.48%. After integrating the AR nanopillars on the device, the power conversion efficiency was increased from 0.48% to 0.61% under irradiation from the normal direction, which is a 27.0% improvement. The improvement in PCE comes mainly from the higher short circuit current density, while the open circuit voltage remains the same with AR nanopillars. This demonstrates that the improvement in conversion efficiency is attributed to more injected light being coupled into the device by using the AR nanopillars. The relative enhancement ratios of the luminous and conversion efficiencies as a function of the incident angle are shown in Fig. 7 for the bifunctional devices covered with the AR nanopillars. The improvement in luminance becomes more significant at larger incident angles with a maximum improvement of 79.7% at 50°. In addition, the conversion efficiency ratio is enhanced at larger incident angles. It reached as high as a 44.4% relative improvement, which was due to better light transmittance and a larger effective light path at large incident angles.²⁷

Increasing image contrast is another important application for this bifunctional device. In order to check the contrast enhancement, we measured the device characteristics under

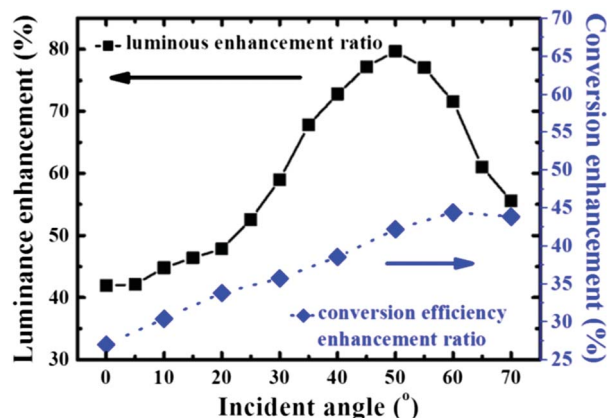


Fig. 7 Luminance and conversion efficiency enhancement ratios of the bifunctional devices with and without AR nanopillars.

dark and bright conditions. The results are shown in Fig. 8. By comparing the device with the reflectance spectra of a reference device with a glass substrate, we found that reflectance between air and the glass substrate was the major problem limiting transparency in the visible wavelength. After attaching the nanopillars onto the device to reduce the ambient reflection on the substrate surface, the average reflectivity was decreased from 12.1% to 5.1%. Moreover, the luminous enhancement

achieved by the additional light extraction from the device substrate can further increase the contrast ratio under bright ambient conditions. Fig. 8(b) shows the contrast ratio of devices driven by different operation voltages under normal indoor lighting. Generally, the contrast ratio in display applications can be expressed as²⁸

$$CR = \frac{L_{on} + L_{ambient}}{L_{off} + L_{ambient}} \quad (1)$$

where L_{on} and L_{off} are the luminous intensity of the turned-on and turned-off pixel, respectively, and $L_{ambient}$ is the luminous intensity of the reflected ambient light. Due to the improvement of luminance and reduction of ambient reflection, the contrast ratio was greatly improved in the device with the AR nanopillars and was about 3.5 times higher than in the device without AR nanopillars attached.

Conclusions

We reported a simple and inexpensive method to optimize the performance of the tandem device by using nanoimprinted AR nanopillars. The nanopillars were made on flexible polycarbonate substrates by using the nanoimprinting method with an anodized aluminum oxide template. The average reflectivity of the patterned polycarbonate substrate at visible wavelengths was reduced to 3.3% in the most optimized case. The AR nanopillars increased both the luminance and power conversion efficiency. With the AR nanopillars attached, the luminance and conversion efficiency were improved by 41.9% and 27.0%, respectively at normal incidence. Moreover, the contrast ratio was also increased up to 3.5 times.

Acknowledgements

This work was supported by National Science Council, Taipei, Taiwan, under Contract no. NSC-101-2627-E-002-005, NSC-100-2120-M-007-006 and NSC-100-2221-E-001-010-MY3.

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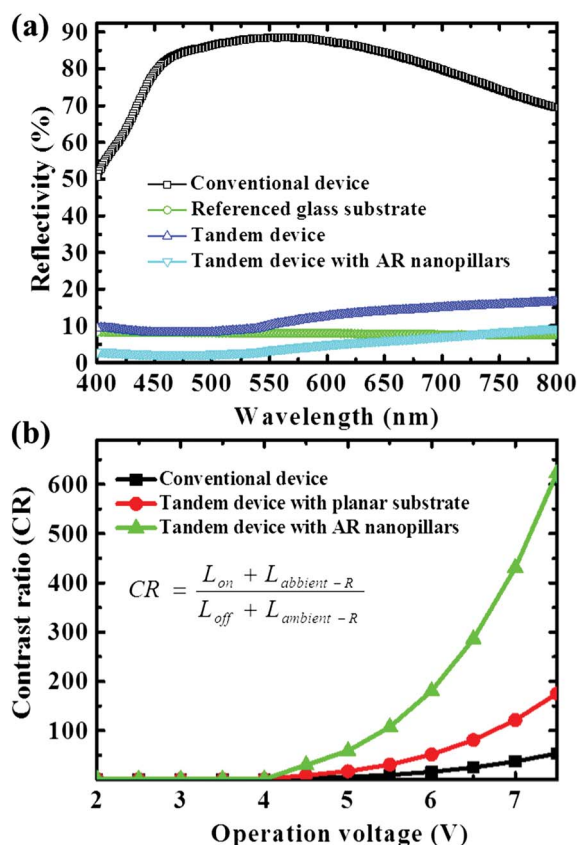


Fig. 8 Reflectance spectra (a) and contrast ratio (b) of reference TOLED and bifunctional devices with and without AR nanopillars.

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